



Six Pillars of Semicon India Programme 2.0

The **Semicon India Programme 2.0** is built on six pillars: **chip design, machines and materials, semiconductor fabrication (fabs), ATMP/OSAT (Assembly, Testing, Marking and Packaging/Outsourced Semiconductor Assembly and Test), research & development, and talent development**. The programme aims to build a self-reliant semiconductor ecosystem.

It seeks to strengthen domestic manufacturing, innovation, supply chains, and skilled manpower, positioning India as a global semiconductor hub.

APSC Relevance: Science & Technology; Economy.